

Complete X- Ray and Gamma-Ray Spectrometer

X-123CdTe

The X-123CdTe is a complete X-Ray Detector & Gamma-Ray System in one small box that fits in your hand.

INCLUDES

- 1 CdTe X-Ray & Gamma-Ray Detector and Preamplifier
- 2 Digital Pulse Processor and MCA
- 3 Power Supply and Interface with PC

Features

- Compact integrated system
- · Simple to operate
- Small size (2.7 x 3.9 x 1 in or 7 x 10 x 2.5 cm)
- Low power (2.5 W)
- · Light weight (6.3 oz or 180 g)
- USB and RS232 Communication

Applications

- Medical X-Ray & Gamma-Ray Detection, Mammography, Radiology, Conventional X-Ray
- X-Ray Fluorescence Instrumentation
- Art and Archaeology
- Portable X-Ray & Gamma-Ray Instruments
- · X-Ray & Gamma-Ray Research
- Teaching
- Nuclear Plant Monitoring
- Uranium and Plutonium Detection

Detector

- CdTe for X-Ray & Gamma-Ray detection
- 2-Stage thermoelectrical cooler
- Area: 9 or 25 mm²
- Thickness: 1 mm

Typical Performance

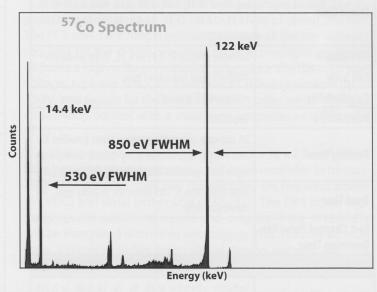
- Resolution at 122 keV < 1.2 keV FWHM
- Optimum energy range: 5 keV to 150 keV
- Max count rate: Up to 2 x 105 cps

Detailed performance depends on detector and configuration, which can be optimized for specific applications.

OEM's #1 Choice 1-2-3 CdTe x-Ray Spectrometer

No Liquid Nitrogen

The X-123CdTe is capable of detecting energies from a few keV to several hundreds of keV.



Overview

The X-123 represents the culmination of 14 years of X-ray detector development at Amptek. Our philosophy has always been to create small, low power, high performance instruments while keeping them simple to operate. The X-123CdTe exemplifies this philosophy by providing in a single package the XR-100T-CdTe X-Ray and Gamma-Ray Detector and its Charge Sensitive Preamplifier; the DP5 Digital Pulse Processor with pulse shaper, MCA, and interface; and the PC5 Power Supply. *All that is needed is a +5 Volts DC input and a USB, Ethernet or RS232 connection to your computer.*

X-123CdTe Specifications

| SYSTEM PERFORMAN | CE | | | | | | |
|-------------------------------------------|-----------------------------------------------------------------------------------------------------|---------------------------------------------------------------------|-----------------------|-----------------------|--|--|--|
| Energy Resolution | 9 mm ² : <1.2 keV FWHM, typical | | | | | | |
| @ 122 keV, ⁵⁷ Co | 25 mm ² : <1.5 keV FWHM, typical | | | | | | |
| Energy Range | 5 to 150 keV. May be used at higher energy with lower efficiency, contact Amptek. | | | | | | |
| Maximum Count Rate | | | | | | | |
| Depends on peaking time. | | ended maxima | for 50% dead | time with pile-up | | | |
| rejection enabled are show | | | | | | | |
| DP5 Peaking Tin | | e (μ s) 2.4 μs 6.4 μs 25.6 μs | | | | | |
| Shaping Tin | ne (µs) | 1.0 µs | 2.9 μs | 11.6 μs | | | |
| Recommended Ma | x Rate | 1.2 x 10 ⁵ | 4.6 x 10 ⁴ | 1.2 x 10 ⁴ | | | |
| DETECTOR AND PREA | MPLIF | IER | | | | | |
| Detector Type | CdTe (a | CdTe (also available with Si-PIN or SDD) | | | | | |
| Detector Area | 9 mm ² or 25 mm ² | | | | | | |
| Detector Thickness | 1 mm | 1 mm | | | | | |
| Be Window Thickness | 4 mil (| 4 mil (100 μm) | | | | | |
| Thermoelectric Cooler | 2-stage | | | | | | |
| Preamplifier Type | Amptek custom design with current feedback. | | | | | | |
| PULSE PROCESSOR | | | | | | | |
| Gain | Combination of coarse and fine gain yields overall gain continuously adjustable from 0.84 to 127.5 | | | | | | |
| | Software selectable settings from 1.12 to 102 in 16 | | | | | | |
| | log steps. 1.12, 2.49, 3.78, 5.26, 6.56, 8.39, 10.10, | | | | | | |
| Coarse Gain | 11.31, 14.56, 17.77, 22.42, 30.83, 38.18, 47.47, | | | | | | |
| | 66.26, 102.0 | | | | | | |
| Fine Gain | Software selectable, 0.75 to 1.25, 10 bit resolution | | | | | | |
| Full Scale | 1000 mV input pulse @ x1 gain | | | | | | |
| Gain Stability | <20 p | <20 ppm / °C (typical) | | | | | |
| Pulse Shape | Trapez | zoidal | | | | | |
| | 24 software selectable peaking times between 0.8 | | | | | | |
| Peaking Time | and 102 µs, approximately log spaced, corresponding to some gaussian shaping times of 0.4 to 45 µs. | | | | | | |
| | ing to semi-gaussian shaping times of 0.4 to 45 µs. | | | | | | |
| Dead Time | | Total dead time is 1.05 times the peaking time. No conversion time. | | | | | |
| Fast Channel Pulse Pair Resolving Time | 120 ns | | | | | | |
| MCA | | | | | | | |
| IIIGA | Softw | are selectable | to 8k 4k 2k | 1k 0.5k or 0.25k | | | |
| Number of Channels | Software selectable to 8k, 4k, 2k, 1k, 0.5k, or 0.25k channels | | | | | | |
| - | Time, total counts, counts in an ROI, counts in a chan- | | | | | | |
| Presets | nel - | | | | | | |
| COMMUNICATIONS | | | | | | | |
| USB | 2.0 fu | II-speed (12 M | lbps) | | | | |
| | | | | | | | |

Standard RS232 at 115.2 k or 57.6 Kbaud

10 base-T

Serial Ethernet

| POWER | | | | | | | | | | |
|------------------------------------------------|------------------------------|------------------------|--------|--------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------|---------------------------------------------------------|----------------------------------------------|--|--|--|
| Nominal Input | | | | $+5$ VDC at 500 mA (2.5 W) (typical). Current depends strongly on detector ΔT . Ranges from 300 to 800 mA at 5 VDC. AC adapter provided. | | | | | | |
| Input Range | | | | 4 V to 6 V (300 to 200 mA, 500 mA max)) | | | | | | |
| High Voltage Supply | | | | Internal multiplier, set to 500 V, adjustable to 750 V | | | | | | |
| Cooler Supply | | | | Closed loop controller with ΔT _{max} = 85°C | | | | | | |
| GENER | AL an | d ENVII | 20 | | | | | | | |
| Operating Temperature | | | | -20 °C to +50 °C | | | | | | |
| Warranty Period | | | 1 year | | | | | | | |
| Typical Device Lifetime | | | | 5 to 10 years, depending on use | | | | | | |
| Storage and Shipping | | | | Typical: -20 °C to +50 °C, 10 to 90% humidity non- condensing Long-term storage: 10+ years in dry environment | | | | | | |
| Compliance | | | 7 | RoHS Compliant | | | | | | |
| TÜVRheinland | | | | TUV Certification Certificate #: CU 72101153 01 Tested to: UL 61010-1: 2009 R10.08 CAN/CSA-C22.2 61010-1-04+Gl1 | | | | | | |
| 501111 | - | US | | - | 111, 65,11 | 22.2 01010 1 0 | | | | |
| CONNE | CIOR | 12 | П | <u> </u> | | ICD M: : 4 4 : 1 | | | | |
| USB | | | | Standard USB Mini 1.1 jack | | | | | | |
| RS232 Standard | 2.5 m | m stereo a | udi | io ja | ack | | | | | |
| Tip | T | Transmit | | | To PC Receive DB9 pin 2 (DB25 pin 3) | | | | | |
| Ring | | Receive | | | To PC Transmit DB9 pin 3 (DB25 pin 2) | | | | | |
| Sleeve | 3 | | | To PC Ground DB9 pin 5 (DB25 pin 7) | | | | | | |
| Ethernet | | | ī | Standard Ethernet connector (RJ-45) | | | | | | |
| Power | | | 1 | Hirose MQ172-3PA(55), Mating plug: MQ172-3SA-CV | | | | | | |
| Auxilian 2 x 8 16- cable ass row ever | pin 2 n sembly n pins. | (Samtec F Top right | P/N | Sam TCI | ntec part MD-08-S 1, bottor | t number ASP-13 5-XX.XX-01). Top m right pin = 2. | 5096-01). Mates with row odd pins, bottom | | | |
| Pin# | | lame | | | Pin# | Name | Beach was at \$1 | | | |
| 1 | SCA1 | | | | 2 | SCA2 | Optimum discr | | | |
| 3 | SCA3 | | | | 4 | SCA4 | | | | |
| 5 | SCA5 | | | | 6 | SCA6 | mohaq salleral | | | |
| 7 | SCA7 | | | | 8 | SCA8 | nea namer, notic | | | |
| 9 | | _IN1 | | | 10 | AUX_OUT_1 | | | | |
| 11 | - | _IN2 | | | 12 | AUX_OUT_2 | Assistant the | | | |
| 13 | 102 | | | | 14 | 103 | | | | |
| 15 | GND | | | | 16 | GND | | | | |

X-123CdTe Interface Software

ADMCA

The X-123CdTe can be controlled by the Amptek ADMCA display and acquisition software. This software completely controls and configures the X-123CdTe, and downloads and displays the data. It and supports regions of interest (ROI), calibrations, peak searching, and so on. The ADMCA software includes a seamless interface to the XRF-FP quantitative X-ray analysis software package. Runs under Windows 98SE or later (32-bit only) on PC compatible computers. Windows XP PRO SP2 or later recommended.

DPP API

The X-123CdTe comes with an Application Programming Interface (API) in the form of a DLL library. The user can use this library to easily write custom code to control the X-123CdTe for custom applications or to interface it to a larger system. Examples are provided in VB, VC++, etc. on how to use the API. A Window CE/Pocket PC version is also provided.

VB Demonstration Software

The VB demonstration software runs on a personal computer and permits the user to set the X-123CdTe parameters, to start and stop data acquisition, and to save data files. It is provided with source code and can be modified by the user. This software is intended as an example of how to manually control the X-123CdTe through either the USB or RS-232 interface using the most basic calls without the DPP API. This is primarily needed as an example when writing software for non-Windows platforms.

X-123CdTe Description

Amptek's specialty is X-ray spectrometers, which are small, low power, high performance, and simple to operate. The X-123CdTe combines in a single package Amptek's standard, high performance X-ray spectroscopy components: the XR100T-CdTe detector and preamplifier, DP5 digital pulse processor and MCA, and PC5 power supply. The result is a complete integrated system which can fit in your hand. In many commercially available systems, the preamplifier alone has more size, mass, and power than this integrated system. It requires only 2 connections to run: +5 VDC power and a standard RS-232 or USB bus. With the X-123CdTe, anyone can rapidly obtain high quality X-ray and Gamma-ray spectra.

X-rays & Gamma-rays interact with CdTe atoms to create an average of one electron/hole pair for every 4.43 eV of energy lost in the CdTe. Depending on the energy of the incoming radiation, this energy loss is dominated by either the photoelectric effect or Compton scattering. The probability or efficiency of the detector to "stop" the incoming radiation and create electron/hole pairs increases with the thickness of CdTe.

The detector is mounted on a thermoelectric cooler along with the input FET and coupled to a custom charge sensitive preamplifier. The thermoelectric cooler reduces the electronic noise in the detector and preamplifier, but the cooling is transparent to the user: it operates like a room temperature system.

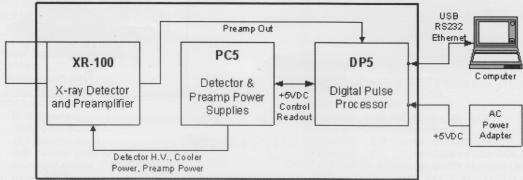
The pulse processor is the DP5, a digital pulse processor which replaces both the shaping amplifier and multichannel analyzer (MCA) found in most analog systems. The use of digital technology improves several key parameters: (1) better performance, specifically better resolution and operation at higher

count rates; (2) greater flexibility since more configuration options are available and they are selected by software over a RS-232 interface, and (3) improved stability and reproducibility. The DP5 digitizes the preamplifier output, applies real-time digital processing to the signal, detects the peak amplitude (digitally), and bins this value in its histogramming memory, generating an energy spectrum. The spectrum is then transmitted over the DP5's interface to the user's computer. The Amptek DP5 has 6 main function blocks to implement these functions: (1) an analog prefilter; (2) an ADC; (3) a digital pulse shaper; (4) pulse selection logic; (5) histogram logic, and (6) interfacing hardware (which includes a microcontroller) and software.

The power supply is Amptek's PC5, a single board. The input is approximately +5 VDC with a current of about 250 mA. The PC5 uses switching supplies to produce all the low voltages required for the digital processor and the preamplifier. It also includes a high voltage multiplier to produce the detector bias voltage, typically 500 V for 1 mm thick CdTe, but variable up to 750 V, and supply for the thermoelectric cooler which provides closed loop control with a maximum temperature differential of 85 °C.

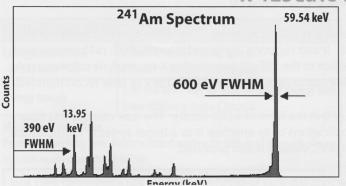
The complete system is packaged in $7 \times 10 \times 2.5 \text{ cm}^3$ aluminum box, with the detector mounted on an extender. In its standard configuration, only two connections are required: power (+5 VDC) and serial (either USB or RS232). The DP5 board supports several additional inputs and outputs, if the X-123CdTe will be integrated with other equipment. This includes an MCA gate, a memory buffer select signal, timing outputs, and SCA ouputs.

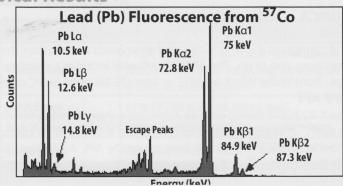
X-123 Architecture and Connection Diagram

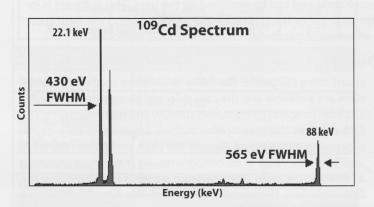


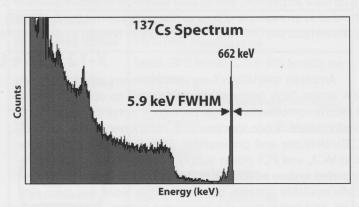
For full system specifications, please see http://www.amptek.com

X-123CdTe Typical Results

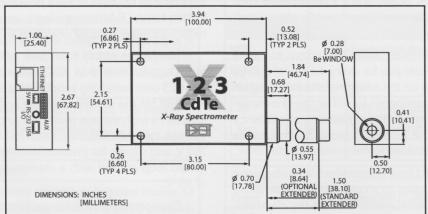




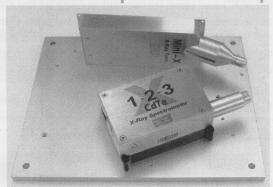




X-123CdTe Mechanical Dimensions

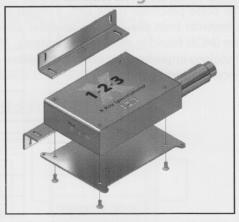


X-123CdTe Experimental Set-up



The X-123CdTe and Mini-X on the MP1 mounting plate.





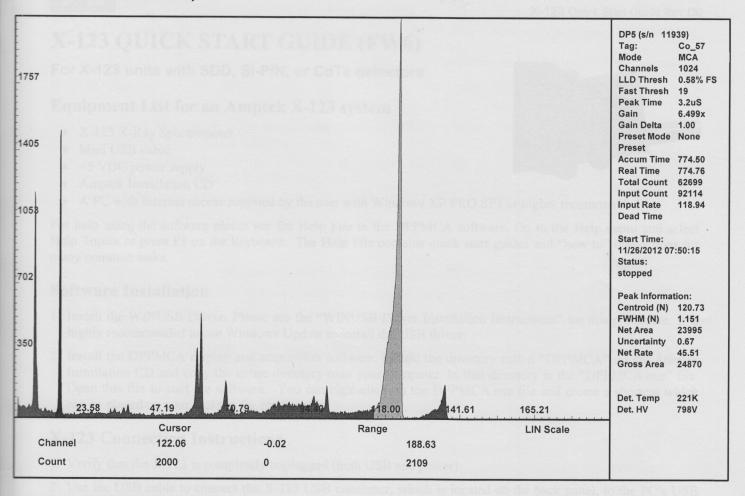
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X-123 S/N: X011939, AXR-CDTE S/N: 118850

Window Thickness - Be 4mil, Temperature Sensor - Diode, Collimator - No Internal, Cooler Stage - 2 stage,

Detector Thickness - 1mm, Detector Area - 9mm2, Detector Type - CdTe, Feedback Type - T,

Assembly Type - 1.5 inch EXT

T = 221.0 Degrees K, FWHM@120.728keV:1.151keV, Tested with 3.2uS Peak Time, HV Bias: 800.0 Volts,

Amptek Code # ZY-TC2470D-G4BB

Configuration

CLCK=80; 20MHz/80MHz GAIF=0.9913; Fine Gain

RESL=3276; Detector Reset Lockout TPFA=50; Fast Channel Peaking Time

RTDE=OFF: RTD On/Off MCAC=1024; MCA/MCS Channels AINP=NEG; Analog Input Pos/Neg

GAIA=5; Analog Gain Index

PDMD=NORM; Peak Detect Mode (Min/Max)

TLLD=OFF; LLD Threshold DACO=OFF; DAC Output RTDS=0; RTD Sensitivity BLRM=1; BLR Mode BLRU=0; BLR Up Correction AUO1=ICR; AUX_OUT Selection PRER=OFF; Preset Real Time

PRCL=0; Preset Counts Low Threshold

HVSE=800; HV Set

PAPS=ON; Preamp 8.5/5 (N/A) SCOT=87; Scope Trigger Position MCSL=0; MCS Low Threshold MCST=0.00; MCS Timebase TPMO=OFF; Test Pulser On/Off GPIN=AUX1; G.P. Counter Input GPGA=OFF; G.P. Counter Uses GATE? MCAE=OFF; MCA/MCS Enable

RESC=?; Reset Configuration TPEA=3.200; Peaking Time

GAIN=6.499; Total Gain (Analog * Fine)

TFLA=0.200; Flat Top PURE=ON; PUR Interval On/Off MCAS=NORM; MCA Source SOFF=OFF; Set Spectrum Offset

INOF=DEF; Input Offset CUSP=0; Non-Trapezoidal Shaping

THSL=0.585; Slow Threshold THFA=19.18; Fast Threshold DACF=0; DAC Offset RTDT=0.00; RTD Threshold BLRD=3; BLR Down Correction

GATE=OFF; Gate Control PRET=OFF; Preset Time PREC=OFF; Preset Counts

PRCH=0; Preset Counts High Threshold

TECS=230; TEC Set

SCOE=RI; Scope Trigger Edge SCOG=1; Digital Scope Gain MCSH=0; MCS High Threshold AUO2=ICR; AUX_OUT2 Selection GPED=FA; G.P. Counter Edge

GPME=OFF; G.P. Counter Uses MCA_EN?

GPMC=OFF; G.P. Counter Cleared With MCA Counters? BOOT=ON; Turn Supplies On/Off At Power Up